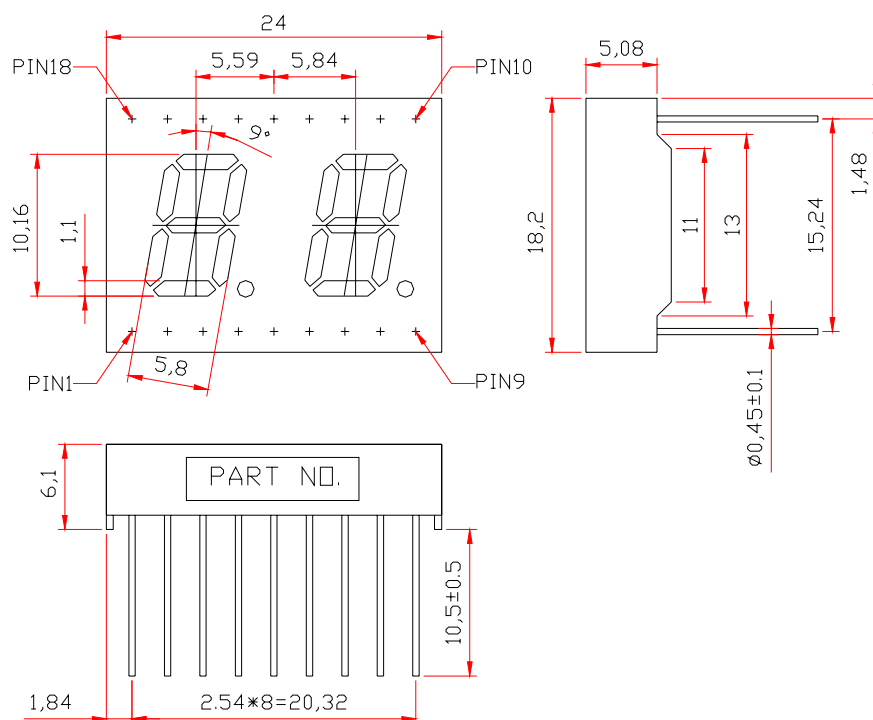


WCN2-0040R5-AA1**SPECIFICATION**

WCN			CUSTOMER Confirmed
Prepared by	Checked by	Approved by	
Fei 2016-03-25	Athena		
REVISION RECORD			

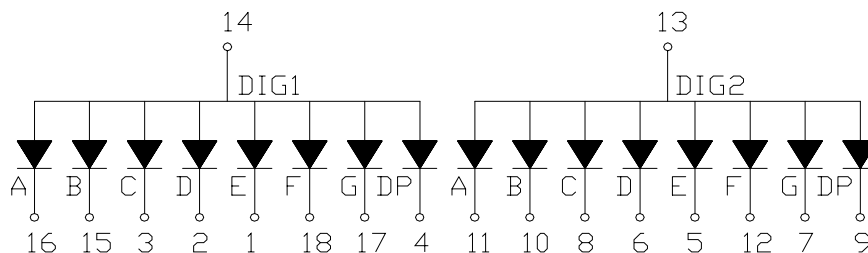
**REVISION: A0**

Outer Dimension:



Note: Unless otherwise stated , The tolerance is ± 0.25 mm.

Circuit Diagram:



Pin Connection:

PIN NO.	CONNECTION	PIN NO.	CONNECTION
1	Cathode Dig1 E	10	Cathode Dig2 B
2	Cathode Dig1 D	11	Cathode Dig2 A
3	Cathode Dig1 C	12	Cathode Dig2 F
4	Cathode Dig1 DP	13	Common Anode Dig2
5	Cathode Dig2 E	14	Common Anode Dig1
6	Cathode Dig2 D	15	Cathode Dig1 B
7	Cathode Dig2 G	16	Cathode Dig1 A
8	Cathode Dig2 C	17	Cathode Dig1 G
9	Cathode Dig2 DP	18	Cathode Dig1 F

WCN Opto Group Co., Limited

■ Features:

- High Reliability
- Color: Red
- Low Power Requirement
- Easy Assembly

■ Description:

- Dual Digit LED Display
- Digit Height: 10.16mm (0.40")
- Black Face and Milky Segment

■ Absolute Maximum Rating (Ta=25°C):

Parameter	Symbol	Condition	Color	Rating	Units
Power Dissipation Per Segment	P _d	—	Red	65	mW
Forward Current Per Segment	I _F	—	Red	25	mA
Peak Forward Current Per Segment	I _{FP}	1/10 Duty 10KHz	Red	100	mA
Reverse Voltage Per Segment	V _R	—	Red	5	V
Operating Temperature Range	Topr	—	—	-40~+85	°C
Storage Temperature Range	Tstg	—	—	-40~+85	°C

■ Electrical/Optical Characteristics Rating(Ta=25°C)

Item	Symbol	Test conditions	Location	Rating			Units
				Min.	Typ.	Max.	
Forward Voltage	V _F	I _F =20mA	Per Segment	1.8	2.0	2.60	V
Reverse Current	I _R	V _R =5V	Per Segment	—	—	100	μA
Luminous Intensity	I _V	I _F =10mA	Per Segment	4001	6600	10500	μcd
Wave Length	λ _P	I _F =20mA	Per Segment	—	650	—	nm
	λ _D				640		
Spectral Line Half Width	△λ	I _F =20mA	Per Segment	—	20	—	nm
Luminous Intensity Matching Ratio (Segment to Segment)	I _{v-m}	I _F =10mA				1.2:1	

■ Luminous Intensity Sorting: (Luminous intensity tolerance: +/-10%)

Rank	Symbol	Condition	Min	Max	Unit
L	L	I _F =10mA	4001	5000	μcd
M	M	I _F =10mA	5001	6100	μcd
N	N	I _F =10mA	6101	7200	μcd
O	O	I _F =10mA	7201	8500	μcd
P	P	I _F =10mA	8501	10500	μcd

■ Soldering Conditions: Soldering Temp. ≤+260°C, Soldering Time. ≤3sec.

(at 2mm Distance from The Case of Reflector Edge)

Typical Optical-Electronic Characteristic Curves

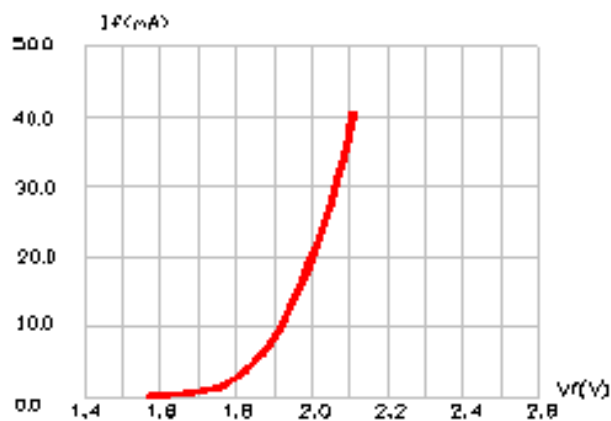


FIG.1 FORWARD CURRENT VS. FORWARD VOLTAGE

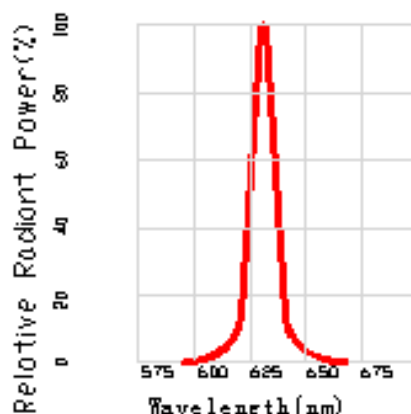


Fig.2 Relative Radiant Power vs. Wavelength

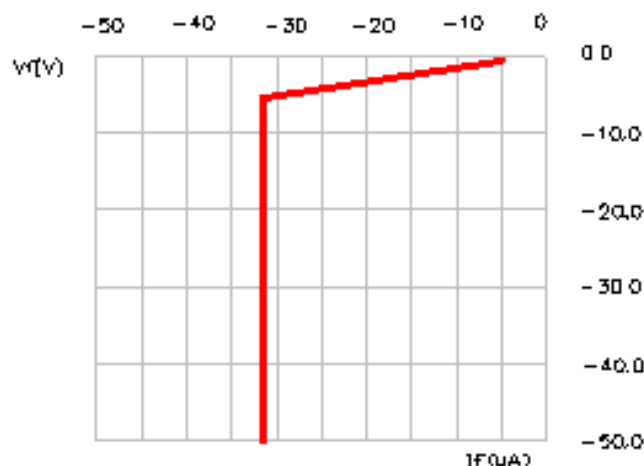


FIG.3 REVERSE CURRENT VS. REVERSE VOLTAGE.

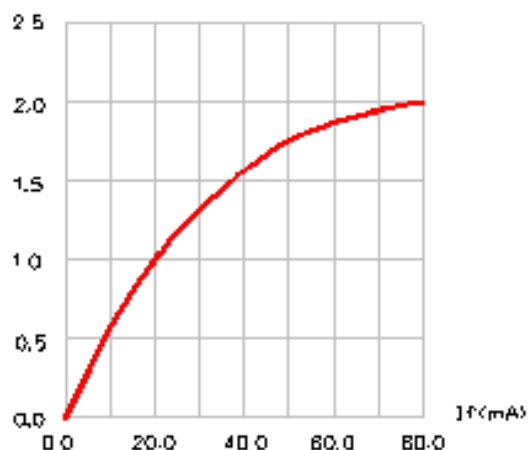


FIG.4 RELATIVE LUMINOUS INTENSITY VS. FORWARD CURRENT.

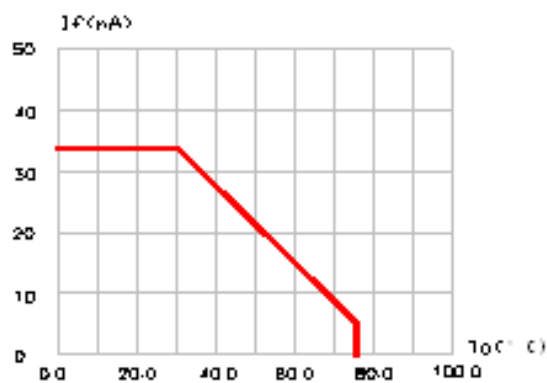


FIG.5 MAXIMUM FORWARD DC CURRENT VS AMBIENT TEMPERATURE (Tjmax=95 °C)

LED Displays Reliability Test:

CLASSIFICATION	TEST ITEM	DESCRIPTION AND TEST CONDITION
ENDURANCE TEST	OPERATION LIFE	EVALUATES RESISTANCE OF THE DEVICE WHEN OPERATED AT ELECTRICAL STRESS T_a = UNDER ROOM TEMPERATURE $I_f = I_{f \max}$
	HIGH TEMPERATURE HIGH HUMIDITY STORAGE	EVALUATES MOISTURE RESISTANCE OF THE DEVICE WHEN STORED FOR A LONG TERM AT HIGH TEMPERATURE AND HUMIDITY $T_a = 65 \pm 5^\circ\text{C}$ RH=90~95%RH TEST TIME=240 $\pm$ 2Hrs
	HIGH TEMPERATURE STORAGE	EVALUATES DEVICE DURABILITY FOR LONG TERM STORAGE IN HIGH TEMPERATURE $T_a = 85 \pm 5^\circ\text{C}$ (COB: $T_a = 65 \pm 5^\circ\text{C}$) TEST TIME=1000Hrs(-24Hrs, +72Hrs)
	LOW TEMPERATURE STORAGE	EVALUATES DEVICE DURABILITY FOR LONG TERM STORAGE IN LOW TEMPERATURE $T_a = -35 \pm 5^\circ\text{C}$ TEST TIME=1000Hrs(-24Hrs, +72Hrs)
ENVIRONMENTAL TEST	TEMPERATURE CYCLING	EVALUATES RESISTANCE OF DEVICE AT THERMAL STRESSES OR EXPANSION AND CONTRACTION $85^\circ\text{C} \sim 25^\circ\text{C} \sim -35^\circ\text{C} \sim 25^\circ\text{C}$ 30min 5min 30min 5min 10 CYCLES(COB: $T_{\text{hot}}=65^\circ\text{C}$, $T_{\text{cold}}=-25^\circ\text{C}$)
	THERMAL SHOCK	EVALUATES DEVICE STRUCTURE AND STRUCTURE AND MECHANICAL RESISTANCE WHEN SUDDENLY EXPOSED AT SERVE CHANGES $85 \pm 5^\circ\text{C} \sim -35 \pm 5^\circ\text{C}$ 10min 10min 10 CYCLES(COB: $T_{\text{hot}}=65^\circ\text{C}$, $T_{\text{cold}}=-25^\circ\text{C}$)
	SOLDERABILITY	EVALUATES SOLDERABILITY ON LEADS OF DEVICE $T_{\text{SOL}}=230 \pm 5^\circ\text{C}$ DWELL TIME=5 $\pm$ 1sec.
	SOLDER RESISTANCE	EVALUATES RESISTANCE TO THERMAL STRESS CAUSED BY SOLDERING $T_{\text{SOL}}=260 \pm 5^\circ\text{C}$ DWELL TIME=10 $\pm$ 1sec.

Package Pattern:

104 pcs / Expandable Polyethylene.

520 pcs / Box(360*175*130mm).

3120 pcs / Catton(550*380*280mm).